



Test Report: LCM-40U

35W Multiple-Stage Constant Current Mode LED Driver

■ DESIGN VERIFY TEST

- Output Function Test
- Input Function Test
- Protection Function Test
- Control Function Test
- Component Stress Test

■ SAFETY & E.M.C. TEST

- Safety Test
- E.M.C. Test

■ RELIABILITY TEST

- ENVIRONMENT TEST

■ DESIGN VERIFY TEST

OUTPUT FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT	VERDICT
1	DC VOLTAGE RANGE	350mA : 2~100V 500mA : 2~70V 600mA : 2~59V 700mA : 2~50V 900mA : 2~39V 1050mA : 2~34V	I/P : 115 VAC O/P : LED LOAD Ta : 25°C	350mA : 2~100V 500mA : 2~70V 600mA : 2~59V 700mA : 2~50V 900mA : 2~39V 1050mA : 2~34V	P
2	OPEN CIRCUIT VOLTAGE	350mA : 110V 500mA : 110V 600mA : 110V 700mA : 65V 900mA : 65V 1050mA : 65V	I/P : 115 VAC O/P : NO LOAD Ta : 25°C	350mA : 101.68 V 500mA : 71.07 V 600mA : 60.17 V 700mA : 51.19 V 900mA : 40.26 V 1050mA : 35.10 V	P
3	CURRENT RIPPLE	5.0%(max).@ rated current	I/P : 115VAC O/P : LED LOAD Ta : 25°C	350mA : <5 % 500mA : <5 % 600mA : <5 % 700mA : <5 % 900mA : <5 % 1050mA : <5 %	P
4	CURRENT TOLERANCE	±5%	I/P : 115VAC O/P : LED MIN~ LED MAX Ta : 25°C	350mA : <5 % 500mA : <5 % 600mA : <5 % 700mA : <5 % 900mA : <5 % 1050mA : <5 %	P
5	AUXILIARY DC OUTPUT	Nominal 12V (deviation 11.4~12.6V) @ 50mA	I/P : 115 VAC O/P : LED LOAD Ta : 25°C	12.085V/115V	P
6	SET UP TIME	115VAC : 1000 ms (Max)	I/P : 115 VAC O/P : LED LOAD Ta : 25°C	115VAC/ 268 ms	P

INPUT FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT	VERDICT
1	INPUT VOLTAGE RANGE	90VAC~132 VAC	I/P : TESTING O/P : LED LOAD Ta : 25°C	90 V~132V	P
			I/P : LOW-LINE-3V= 87 V (PLEASE CHECK DERATING CURVE) HIGH-LINE+10V=142 V O/P : LED LOAD ON : 30 Sec. OFF : 30 Sec 10MIN (AC POWER ON/OFF NO DAMAGE)	TEST : OK	
2	INPUT FREQUENCY RANGE	47HZ ~63 HZ NO DAMAGE	I/P : 90 VAC ~ 132 VAC O/P : LED LOAD Ta : 25°C	TEST : OK	P
3	POWER FACTOR	0.98 / 115 VAC(TYP)	I/P : 115 VAC O/P : LED LOAD Ta : 25°C	PF= 0.993	P
4	EFFICIENCY	87.5% (TYP)	I/P : 115 VAC O/P : LED 500mA/70V Ta : 25°C	87.64 %	P
5	INPUT CURRENT	115V/ 0.43 A (TYP)	I/P : 115 VAC O/P : LED LOAD Ta : 25°C	I = 0.347 A	P
6	INRUSH CURRENT	115V/ 15 A (TYP) (twidth=270us measured at 50% Ipeak) COLD START	I/P : 115 VAC O/P : LED LOAD Ta : 25°C	I = 13.8 A T50= 234 us	P
7	LEAKAGE CURRENT	< 0.5 mA / 120 VAC	I/P : 120 VAC O/P : NO LOAD Ta : 25°C	L-FG : 0.0012 mA N-FG : 0.0013 mA	P
8	TOTAL HARMONIC DISTORTION	Total harmonic distortion will be lower than 20% when output loading is 50% or higher	I/P : 115 VAC O/P : 50% LOAD	THD : 13.45%	P

PROTECTION FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT	VERDICT
1	OVER VOLTAGE PROTECTION	110 V ~ 130 V	I/P : 115 VAC O/P : NO LOAD Ta : 25°C	121.86V/ 115 VAC Shut down o/p voltage, re- power on to recover	P
2	OVER TEMPERATURE PROTECTION	NO DAMAGE	I/P : 115 VAC O/P : LED LOAD	O.T.P. Active Shut down o/p voltage, re- power on to recover	P
3	SHORT PROTECTION	SHORT EVERY OUTPUT 1 HOUR NO DAMAGE	I/P : 132 VAC O/P : LED LOAD Ta : 25°C	NO DAMAGE Constant Current Limiting, recovers automatically after fault condition is removed	P

CONTROL FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT	VERDICT																																																								
1	DIP Switch Table	<p>LCM-40U is a multiple-stage constant current driver, selection of output current through DIP switch is exhibited below.</p> <table border="1"> <thead> <tr> <th>Io</th> <th>DIP S.W.</th> <th>1</th> <th>2</th> <th>3</th> <th>4</th> <th>5</th> <th>6</th> </tr> </thead> <tbody> <tr> <td>350mA</td> <td></td> <td>----</td> <td>----</td> <td>----</td> <td>----</td> <td>----</td> <td>----</td> </tr> <tr> <td>500mA</td> <td>ON</td> <td>----</td> <td>----</td> <td>----</td> <td>----</td> <td>----</td> <td>----</td> </tr> <tr> <td>600mA</td> <td>ON</td> <td>ON</td> <td>----</td> <td>----</td> <td>----</td> <td>----</td> <td>----</td> </tr> <tr> <td>700mA(factory default)</td> <td>ON</td> <td>ON</td> <td>ON</td> <td>----</td> <td>----</td> <td>ON</td> <td>----</td> </tr> <tr> <td>900mA</td> <td>ON</td> <td>ON</td> <td>ON</td> <td>ON</td> <td>----</td> <td>ON</td> <td>----</td> </tr> <tr> <td>1050mA</td> <td>ON</td> <td>ON</td> <td>ON</td> <td>ON</td> <td>ON</td> <td>ON</td> <td>ON</td> </tr> </tbody> </table> <p>TEST : OK</p>	Io	DIP S.W.	1	2	3	4	5	6	350mA		----	----	----	----	----	----	500mA	ON	----	----	----	----	----	----	600mA	ON	ON	----	----	----	----	----	700mA(factory default)	ON	ON	ON	----	----	ON	----	900mA	ON	ON	ON	ON	----	ON	----	1050mA	ON	ON	ON	ON	ON	ON	ON			P
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2	TEMPERATURE COMPENSATION OPERATION	<p>SPEC: LCM-40U have the built-in temperature compensation function;by connecting a temperature sensor (NTC resistor) between the +NTC /-NTC terminal of LCM-40U and the detecting point on the lighting system or the surrounding environment, output current of LCM-40U could be correspondingly changed,based on the sensed temperature,to ensure the long life of LED.</p> <p style="text-align: center;">NTC derating curve</p> <p style="text-align: center;">Sensed Temperature(°C)</p> <p>© LCM-40U can still be operated normally when the NTC resistor is not connected and the value of output current will be the current level selected through the DIP switch. © NTC reference:</p> <table border="1"> <thead> <tr> <th>NTC resistance</th> <th>Output Current</th> </tr> </thead> <tbody> <tr> <td>220K</td> <td>< 60°C , 100% of the rated current (corresponds to the setting current level) > 60°C , output current begin to reduce, please refer to the curve for details.</td> </tr> <tr> <td>330K</td> <td>< 70°C , 100% of the rated current (corresponds to the setting current level) > 70°C , output current begin to reduce, please refer to the curve for details.</td> </tr> <tr> <td>470K</td> <td>< 80°C , 100% of the rated current (corresponds to the setting current level) > 80°C , output current begin to reduce, please refer to the curve for details.</td> </tr> </tbody> </table> <p>Notes: 1. MEAN WELL does not offer the NTC resistor and all the data above are measured by using THINKING TTC03 series. 2. If other brands of NTC resistor is applied,please check the temperature curve first.</p> <p>© Dimming and Synchronization function of the driver will be invalid when the "temperature compensation" function is in use TEST CONDITION : I/P : 115 VAC RESULT:OK</p>	NTC resistance	Output Current	220K	< 60°C , 100% of the rated current (corresponds to the setting current level) > 60°C , output current begin to reduce, please refer to the curve for details.	330K	< 70°C , 100% of the rated current (corresponds to the setting current level) > 70°C , output current begin to reduce, please refer to the curve for details.	470K	< 80°C , 100% of the rated current (corresponds to the setting current level) > 80°C , output current begin to reduce, please refer to the curve for details.			P																																																
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<p>3</p>	<p>Dimming OPERATION</p>	<p>SPEC:</p> <ul style="list-style-type: none"> ※ 3 in 1 dimming function · Output constant current level can be adjusted by applying one of the three methodologies between DIM+ and DIM-: 0 ~ 10Vdc, or 10V PWM signal or resistance. · Direct connecting to LEDs is suggested. It is not suitable to be used with additional drivers. · Dimming source current from power supply: 100uA(typ.) DO NOT connect "DIM-" to "-V". <p>Reference resistance value for output current adjustment (Typical)</p> <table border="1"> <tr> <th rowspan="2">Resistance value</th> <th>Single driver</th> <th>Short</th> <th>10K Ω</th> <th>20K Ω</th> <th>30K Ω</th> <th>40K Ω</th> <th>50K Ω</th> <th>60K Ω</th> <th>70K Ω</th> <th>80K Ω</th> <th>90K Ω</th> <th>100K Ω</th> <th>OPEN</th> </tr> <tr> <td>Multiple drivers (N=driver quantity for synchronized dimming operation)</td> <td>Short</td> <td>10K Ω /N</td> <td>20K Ω /N</td> <td>30K Ω /N</td> <td>40K Ω /N</td> <td>50K Ω /N</td> <td>60K Ω /N</td> <td>70K Ω /N</td> <td>80K Ω /N</td> <td>90K Ω /N</td> <td>100K Ω /N</td> <td>.....</td> </tr> <tr> <td colspan="2">Percentage of rated current</td> <td>0%</td> <td>10%</td> <td>20%</td> <td>30%</td> <td>40%</td> <td>50%</td> <td>60%</td> <td>70%</td> <td>80%</td> <td>90%</td> <td>100%</td> <td>100%~108%</td> </tr> </table> <p>※ 0 ~ 10V dimming function for output current adjustment (Typical)</p> <table border="1"> <tr> <th>Dimming value</th> <td>0V</td> <td>1V</td> <td>2V</td> <td>3V</td> <td>4V</td> <td>5V</td> <td>6V</td> <td>7V</td> <td>8V</td> <td>9V</td> <td>10V</td> <td>OPEN</td> </tr> <tr> <th>Output current</th> <td>0%</td> <td>10%</td> <td>20%</td> <td>30%</td> <td>40%</td> <td>50%</td> <td>60%</td> <td>70%</td> <td>80%</td> <td>90%</td> <td>100%</td> <td>100%~108%</td> </tr> </table> <p>※ 10V PWM signal for output current adjustment (Typical) : Frequency range : 100Hz~3KHz</p> <table border="1"> <tr> <th>Duty value</th> <td>0%</td> <td>10%</td> <td>20%</td> <td>30%</td> <td>40%</td> <td>50%</td> <td>60%</td> <td>70%</td> <td>80%</td> <td>90%</td> <td>100%</td> <td>OPEN</td> </tr> <tr> <th>Output current</th> <td>0%</td> <td>10%</td> <td>20%</td> <td>30%</td> <td>40%</td> <td>50%</td> <td>60%</td> <td>70%</td> <td>80%</td> <td>90%</td> <td>100%</td> <td>100%~108%</td> </tr> </table> <p>Note: 1. Min. dimming level is about 6% and the output current is not defined when $0\% < I_{out} < 6\%$. 2. The output current could drop down to 0% when dimming input is about 0KΩ or 0Vdc, or 10V PWM signal with 0% duty cycle. 3. Please do not activate "temperature compensation" when performing dimming operation.</p> <p>TEST RESULT :</p> <p>I/P : 115 VAC ; Ta : 25°C</p> <p>Reference resistance value for output current adjustment (Typical)</p> <table border="1"> <tr> <th>Current</th> <th>Short</th> <th>10KΩ</th> <th>20KΩ</th> <th>30KΩ</th> <th>40KΩ</th> <th>50KΩ</th> <th>60KΩ</th> <th>70KΩ</th> <th>80KΩ</th> <th>90KΩ</th> <th>100KΩ</th> <th>OPEN</th> </tr> <tr> <td>350mA</td> <td>0%</td> <td>12.29 %</td> <td>22.29 %</td> <td>32.57 %</td> <td>42.57 %</td> <td>52.86 %</td> <td>62.86 %</td> <td>72.86 %</td> <td>83.14 %</td> <td>93.14 %</td> <td>103.14 %</td> <td>101.14 %</td> </tr> <tr> <td>1050mA</td> <td>0%</td> <td>12.48 %</td> <td>22.57 %</td> <td>32.76 %</td> <td>42.95 %</td> <td>53.05 %</td> <td>63.14 %</td> <td>73.33 %</td> <td>83.62 %</td> <td>93.62 %</td> <td>103.81 %</td> <td>102.67 %</td> </tr> </table> <p>0 ~ 10V dimming function for output current adjustment (Typical)</p> <table border="1"> <tr> <th>Current</th> <th>0V</th> <th>1V</th> <th>2V</th> <th>3V</th> <th>4V</th> <th>5V</th> <th>6V</th> <th>7V</th> <th>8V</th> <th>9V</th> <th>10V</th> <th>OPEN</th> </tr> <tr> <td>350mA</td> <td>0%</td> <td>12.00 %</td> <td>22.00 %</td> <td>32.00 %</td> <td>42.29 %</td> <td>52.29 %</td> <td>62.29 %</td> <td>72.29 %</td> <td>82.29 %</td> <td>92.29 %</td> <td>102.29 %</td> <td>101.14 %</td> </tr> <tr> <td>1050mA</td> <td>0%</td> <td>12.38 %</td> <td>22.38 %</td> <td>32.48 %</td> <td>42.57 %</td> <td>52.57 %</td> <td>62.76 %</td> <td>72.76 %</td> <td>82.86 %</td> <td>92.95 %</td> <td>103.05 %</td> <td>102.67 %</td> </tr> </table> <p>10V PWM signal for output current adjustment (Typical): Frequency range : 100Hz ~ 3KHz</p> <table border="1"> <tr> <th>Current</th> <th>0%</th> <th>10%</th> <th>20%</th> <th>30%</th> <th>40%</th> <th>50%</th> <th>60%</th> <th>70%</th> <th>80%</th> <th>90%</th> <th>100%</th> <th>OPEN</th> </tr> <tr> <td>350mA</td> <td>0%</td> <td>13.71 %</td> <td>23.71 %</td> <td>33.14 %</td> <td>43.14 %</td> <td>52.57 %</td> <td>62.57 %</td> <td>72.00 %</td> <td>82.00 %</td> <td>91.43 %</td> <td>100.29 %</td> <td>101.14 %</td> </tr> <tr> <td>1050mA</td> <td>0%</td> <td>14.10 %</td> <td>24.00 %</td> <td>33.52 %</td> <td>43.52 %</td> <td>52.95 %</td> <td>62.95 %</td> <td>72.57 %</td> <td>82.48 %</td> <td>92.00 %</td> <td>100.76 %</td> <td>102.67 %</td> </tr> </table>	Resistance value	Single driver	Short	10K Ω	20K Ω	30K Ω	40K Ω	50K Ω	60K Ω	70K Ω	80K Ω	90K Ω	100K Ω	OPEN	Multiple drivers (N=driver quantity for synchronized dimming operation)	Short	10K Ω /N	20K Ω /N	30K Ω /N	40K Ω /N	50K Ω /N	60K Ω /N	70K Ω /N	80K Ω /N	90K Ω /N	100K Ω /N	Percentage of rated current		0%	10%	20%	30%	40%	50%	60%	70%	80%	90%	100%	100%~108%	Dimming value	0V	1V	2V	3V	4V	5V	6V	7V	8V	9V	10V	OPEN	Output current	0%	10%	20%	30%	40%	50%	60%	70%	80%	90%	100%	100%~108%	Duty value	0%	10%	20%	30%	40%	50%	60%	70%	80%	90%	100%	OPEN	Output current	0%	10%	20%	30%	40%	50%	60%	70%	80%	90%	100%	100%~108%	Current	Short	10KΩ	20KΩ	30KΩ	40KΩ	50KΩ	60KΩ	70KΩ	80KΩ	90KΩ	100KΩ	OPEN	350mA	0%	12.29 %	22.29 %	32.57 %	42.57 %	52.86 %	62.86 %	72.86 %	83.14 %	93.14 %	103.14 %	101.14 %	1050mA	0%	12.48 %	22.57 %	32.76 %	42.95 %	53.05 %	63.14 %	73.33 %	83.62 %	93.62 %	103.81 %	102.67 %	Current	0V	1V	2V	3V	4V	5V	6V	7V	8V	9V	10V	OPEN	350mA	0%	12.00 %	22.00 %	32.00 %	42.29 %	52.29 %	62.29 %	72.29 %	82.29 %	92.29 %	102.29 %	101.14 %	1050mA	0%	12.38 %	22.38 %	32.48 %	42.57 %	52.57 %	62.76 %	72.76 %	82.86 %	92.95 %	103.05 %	102.67 %	Current	0%	10%	20%	30%	40%	50%	60%	70%	80%	90%	100%	OPEN	350mA	0%	13.71 %	23.71 %	33.14 %	43.14 %	52.57 %	62.57 %	72.00 %	82.00 %	91.43 %	100.29 %	101.14 %	1050mA	0%	14.10 %	24.00 %	33.52 %	43.52 %	52.95 %	62.95 %	72.57 %	82.48 %	92.00 %	100.76 %	102.67 %	<p>P</p>
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<p>4</p>	<p>SYNCHRONIZATION OPERATION</p>	<p>SPEC:</p> <ul style="list-style-type: none"> · Synchronization up to 10 drivers (1 master + 9 slaves) · Dimming operating range : 10%~100% · Sync cable length : < 5m · Sync cable type : Flat cable · Sync cable cross section area : 22 - 24 AWG (0.2-0.3mm²) <p>TEST CONDITION : I/P : 115 VAC RESULT: OK</p>	<p>P</p>																																																																																																																																																																																																																		

COMPONENT STRESS TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT	VERDICT
1	Power Transistor (D to S) or (C to E) Peak Voltage	U2 Rated 530V/5.2A	I/P : High-Line +3V = 135 V O/P : (1)LED LOAD Turn on (2) Output Short (3)LED LOAD continue Ta : 25°C	(1) 460 V (2) 466 V (3) 458 V	P
2	Diode Peak Voltage	D100 Rated 300V/10A	I/P : High-Line +3V = 135 V O/P : (1)LED LOAD Turn on (2)Output Short (3)LED LOAD continue Ta : 25°C	(1) 220 V (2) 11.4 V (3) 216 V	P
3	Input Capacitor Voltage	C5 Rated: 22u/450V	I/P : High-Line +3V = 135 V O/P : (1)LED LOAD Turn on /Off (2) Min load Turn on /Off (3) Burn-IN 1Hour Ta : 25°C	(1) 448 V (2) 442 V (3) 446 V	P
4	Control IC Voltage Test	U2 Rated 15V	I/P : High-Line +3V = 135 V O/P : (1)LED LOAD Turn on /Off (2) Output Short (3) LED LOAD continue Ta : 25°C	(1) 13.5 V (2) 13.5 V (3) 13.5 V	P
5	Power Transistor (D to S) or (C to E) Peak Voltage	Q1 Rated 7.3A/600V	I/P : High-Line +3V = 135 V O/P : (1)LED LOAD Turn on (2) Output Short (3)LED LOAD continue Ta : 25°C	(1) 458 V (2) 444 V (3) 444 V	P

■ SAFETY & E.M.C. TEST

SAFETY TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT	VERDICT
1	WITHSTAND VOLTAGE	I/P-O/P : 3.75 KVAC/min	I/P-O/P : 4.2 KVAC/min Ta : 25°C	I/P-O/P : 1.153 mA NO DAMAGE	P

E.M.C TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT	VERDICT
1	CONDUCTION	FCC part 15 CLASS B	I/P : 115 VAC (50HZ) O/P : LED LOAD Ta : 25°C	PASS Test by certified Lab	P
2	RADIATION	FCC part 15 CLASS B	I/P : 115 VAC (50HZ) O/P : LED LOAD Ta : 25°C	PASS Test by certified Lab	P

■ RELIABILITY TEST

ENVIRONMENT TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT	VERDICT																																																																												
1	TEMPERATURE RISE TEST	MODEL : LCM-40U DIP switch : 1050mA 1. ROOM AMBIENT BURN-IN : 2 HRS I/P : 115VAC O/P : LED LOAD Ta=28.8 °C 2. HIGH AMBIENT BURN-IN : 2 HRS I/P : 115VAC O/P : LED LOAD Ta= 42.6 °C																																																																															
				<table border="1"> <thead> <tr> <th>NO</th> <th>Position</th> <th>ROOM AMBIENT Ta= 28.8 °C</th> <th>HIGH AMBIENT Ta= 42.6 °C</th> </tr> </thead> <tbody> <tr><td>1</td><td>LF1</td><td>58.9°C</td><td>70.8°C</td></tr> <tr><td>2</td><td>BD1</td><td>82.2°C</td><td>92.6°C</td></tr> <tr><td>3</td><td>L2</td><td>71.5°C</td><td>82.3°C</td></tr> <tr><td>4</td><td>C5</td><td>71.9°C</td><td>82.7°C</td></tr> <tr><td>5</td><td>C61</td><td>77.9°C</td><td>88.7°C</td></tr> <tr><td>6</td><td>U2</td><td>82.2°C</td><td>93.7°C</td></tr> <tr><td>7</td><td>D91</td><td>72.7°C</td><td>84.5°C</td></tr> <tr><td>8</td><td>C37</td><td>68.5°C</td><td>80.0°C</td></tr> <tr><td>9</td><td>C92</td><td>78.0°C</td><td>89.2°C</td></tr> <tr><td>10</td><td>RTH2</td><td>69.9°C</td><td>81.9°C</td></tr> <tr><td>11</td><td>T1</td><td>103.6°C</td><td>115.4°C</td></tr> <tr><td>12</td><td>C203</td><td>65.0°C</td><td>76.3°C</td></tr> <tr><td>13</td><td>C105</td><td>59.2°C</td><td>70.4°C</td></tr> <tr><td>14</td><td>Q1</td><td>73.8°C</td><td>85.1°C</td></tr> <tr><td>15</td><td>D8</td><td>75.4°C</td><td>86.4°C</td></tr> <tr><td>16</td><td>D100</td><td>83.0°C</td><td>93.4°C</td></tr> <tr><td>17</td><td>U905</td><td>62.9°C</td><td>74.3°C</td></tr> <tr><td>18</td><td>CASE</td><td>53.9°C</td><td>65.5°C</td></tr> </tbody> </table>	NO	Position	ROOM AMBIENT Ta= 28.8 °C	HIGH AMBIENT Ta= 42.6 °C	1	LF1	58.9°C	70.8°C	2	BD1	82.2°C	92.6°C	3	L2	71.5°C	82.3°C	4	C5	71.9°C	82.7°C	5	C61	77.9°C	88.7°C	6	U2	82.2°C	93.7°C	7	D91	72.7°C	84.5°C	8	C37	68.5°C	80.0°C	9	C92	78.0°C	89.2°C	10	RTH2	69.9°C	81.9°C	11	T1	103.6°C	115.4°C	12	C203	65.0°C	76.3°C	13	C105	59.2°C	70.4°C	14	Q1	73.8°C	85.1°C	15	D8	75.4°C	86.4°C	16	D100	83.0°C	93.4°C	17	U905	62.9°C	74.3°C	18	CASE	53.9°C	65.5°C	P
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2	LOW TEMPERATURE TURN ON TEST	TURN ON AFTER 2 HOUR	I/P : 132VAC/100VAC O/P : LED LOAD Ta= -35 °C	TEST : OK	P																																																																												
3	HIGH HUMIDITY HIGH TEMPERATURE HIGH VOLTAGE TURN ON TEST	AFTER 12 HOURS IN CHAMBER ON CONTROL 40 °C NO DAMAGE	I/P : 135 VAC O/P : LED LOAD Ta= 40 °C HUMIDITY= 95 %R.H	TEST : OK	P																																																																												
4	TEMPERATURE COEFFICIENT	±0.03 %/°C(0~50°C)	I/P : 115 VAC O/P : LED LOAD	±0.001 %/°C(0~50°C)	P																																																																												

5	STORAGE TEMPERATURE TEST	1. Thermal shock Temperature : -45°C~ +90°C 2. Temperature change rate : 25°C / MIN 3. Dwell time low and high temperature : 30 MIN/EACH 4. Total test cycle : 5 CYCLE 5. Input/Output condition : STATIC	TEST : OK	P
6	THERMAL SHOCK TEST	1. Thermal shock Temperature : -35°C~ +45°C 2. Temperature change rate : 25°C / MIN 3. Dwell time low and high temperature : 30 MIN/EACH 4. Total test cycle : 10 CYCLE 5. Input/Output condition : 115VAC/LED LOAD AC ON/OFF TEST turn on 58sec ; turn off 2sec	TEST : OK	P
7	VIBRATION TEST	1 Carton & 1 Set (1) Waveform : Sine Wave (2) Frequency : 10~500Hz (3) Sweep Time : 10min/sweep cycle (4) Acceleration : 2G (5) Test Time : 60min in each axis (X.Y.Z) (6) Ta : 25°C	TEST : OK	P
8	CAPACITOR LIFE CYCLE	LCM-40U ;SUPPOSE C105 IS THE MOST CRITICAL COMPONENT (1) I/P : 115VAC O/P : LED LOAD Ta= 25 °C LIFE TIME (2) I/P : 115VAC O/P : LED LOAD Ta= 40 °C LIFE TIME (3) I/P : 115VAC O/P : 75% LOAD Ta= 40 °C LIFE TIME (4) I/P : 115VAC O/P : 50% LOAD Ta= 40 °C LIFE TIME	(1) 532152 HRS (2) 225378 HRS (3) 214315 HRS (4) 252604 HRS	P
9	MTBF	Conducted by Parts Stress Analysis Prediction 193.6K hrs min. MIL-HDBK-217F (25°C)		P
10	DMTBF/Accelerated Life Test	Demonstration Mean Time Between Failure(Expected Life) : 30,000 hours @ Tcase 75°C ; 50,000 hours @ Tcase 65°C		P

TEST RESULT	TESTER	REVIEW	APPROVAL
PASS	ZHANGZJ/ZHUOKB	SKY	LIUWY

2009/08/04 A50-F023